

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Fitzgerald et al.

SERIAL NO: 10/603,850

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FOR: COPLANAR INTEGRATION OF LATTICE-MISMATCHED
SEMICONDUCTOR WITH SILICON VIA WAFER BONDING
VIRTUAL SUBSTRATESCommissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CHANGE OF CORRESPONDENCE ADDRESS

Applicant's representative requests that the correspondence address for the above application
be changed to:

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Respectfully submitted,



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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being
deposited with the United States Postal Service on the date shown below with sufficient postage as first
class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450,
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Elizabeth M. Ball

Date:

6-23-05